

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claims 1-2 (canceled).

Claim 3 (currently amended): A process for starting a data processing installation as claimed in claim 220, wherein the control unit is at least one of a binary control unit in which the control function is prescribed by the interconnection of logic circuits, and held in a user-specific, integrated circuit in which logic circuit elements have been interconnected as prescribed by ~~the~~a user in a programming operation.

Claim 4 (currently amended): A process for starting a data processing installation as claimed in claim 220, the process further comprising the steps of:

keeping the processor in a reset state in which no commands are executed, during the bootstrap transfer operation and via the control unit; and

enabling execution of commands, via the control unit, after the bootstrap transfer operation by switching over a reset signal.

Claim 5 (currently amended): A process for starting a data processing installation as claimed in claim 120, the process further comprising at least one of the following steps:

~~storing the program commands, via the bootstrap memory unit, even in the event of failure of a supply voltage for the memory unit;~~

outputting bit positions, via the bootstrap memory unit, of its stored program commands serially or using a plurality of read operations per program command; and

~~setting the storage capacity of the bootstrap memory unit to be less than 256 kilobytes;~~  
~~and~~

defining the bootstrap memory to be an EEPROM.

Claim 6 (currently amended): A process for starting a data processing installation as claimed in claim ~~1~~20, the process further comprising at least one of the following steps:

~~erasing, via the main memory unit, its stored program commands in the event of failure of the supply voltage for the main memory unit;~~

allowing simultaneous input and output, via the main memory unit, of a plurality of bit positions of a program command; and

defining the main memory unit as a synchronously operating dynamic RAM.

Claim 7 (currently amended): A process for starting a data processing installation as claimed in claim ~~1~~20, the process further comprising at least one of the following steps:

~~storing the program commands, via the reload memory unit, even in the event of failure of a supply voltage for the memory unit;~~

outputting, via the reload memory unit, bit positions of its stored program commands serially or using a plurality of read operations per program command;

defining the storage capacity of the reload memory unit to be greater than 4 megabytes; and

incorporating into the memory unit at least one of a "multimedia card", a Compact Flash card, a SmartMedia card, and a Memory Stick memory unit.

Claim 8 (currently amended): A process for starting a data processing installation as claimed in claim ~~1~~20, wherein the reload memory unit contains a register in which a start address of one currently readable memory area from at least two memory areas of the reload memory unit is noted, such that, when the bootstrap program is executed, the transfer operation is executed based on the start address.

Claim 9 (original): A process for starting a data processing installation as claimed in claim 8, the process further comprising the step of:

replacing the program commands in the reload memory unit by storing a new version of the program commands in the currently unreadable memory area of the reload memory unit,

noting in the register the address of the other memory area, initiating a new start operation, re-entering into the register the value entered before the other memory area was set in the event of errors occurring, and initiating a start operation again.

Claim 10 (currently amended): A process for starting a data processing installation as claimed in claim ~~1~~20, the process further comprising the steps of:

changing the address of at least one portion of the program commands, at least once during execution of the program commands transferred to the main memory unit, the program commands being moved from their original memory area in the main memory unit to another memory area of the main memory unit;

starting execution of the program commands, via the processor, stored in the other memory area;

controlling the transfer of program commands, via the processor, in the reload memory unit to the original area; and

switching the processor to a defined initial state, after the transfer operation, by switching over the reset signal.

Claim 11 (currently amended): A process for starting a data processing installation as claimed in claim ~~1~~20, the process further comprising the steps of:

compressing the program command stored in the reload memory unit using a compression process;

storing a decompression process in one of a portion of the program commands in the bootstrap memory unit and an uncompressed portion of the program commands in the reload memory unit; and

compressing the compressed program commands when the program commands for the decompression process are executed.

Claim 12 (original): A process for starting a data processing installation as claimed in claim 10, the process further comprising the steps of:

storing the program commands for the decompression process in the bootstrap memory unit;

setting the processor, after the bootstrap transfer operation, to the defined initial state;

changing the address of at least one portion of the bootstrap program before the reload transfer operation by copying the at least one portion;

storing program commands in the original address range as part of the reload transfer operation; and

setting the processor, after the reload transfer operation, to the defined initial state.

Claim 13 (original): A process for starting a data processing installation as claimed in claim 10, the process further comprising the steps of:

storing the program commands for the compression process in the reload memory unit;

setting the processor, after the bootstrap transfer operation, to the defined initial state;

changing the address of at least one portion of the bootstrap program before the reload transfer operation by copying the at least one portion;

storing program commands for the decompression process in the original address range in a first phase of the reload transfer operation;

setting the processor, after the first phase of the reload transfer operation, to the defined initial state;

changing the address of at least one portion of the program commands for the decompression process by copying the at least one portion;

storing program commands of an operating system, in a second phase of the reload operation, in the original address range; and

setting the processor, after the second phase of the reload operation, to the defined initial state again.

Claim 14 (currently amended): A data processing installation, comprising:

a processor for executing program commands;

a bootstrap memory unit for storing a bootstrap program;

a reload memory unit for storing program commands; and

a main memory unit to which program commands from the reload memory unit are transferred using the bootstrap program before execution by the processor; ;

wherein at least one of the bootstrap memory unit and the reload memory unit is one of a memory unit with serial data access and a memory unit which requires a plurality of read access operations in order to read a program command for the processor; and

a control unit which operates without a program and, when the data processing installation is turned on, transfers the bootstrap program from the bootstrap memory unit to the main memory unit, the bootstrap memory outputting the bit positions of the program commands of the bootstrap program serially or using a plurality of read operations per program command.

Claim 15 (canceled).

Claim 16 (currently amended): A circuit arrangement, comprising:

an interface to a processor, the processor for executing program commands;

an interface to one of a bootstrap memory unit with serial data access and a bootstrap memory unit which requires a plurality of read access operations in order to read a program command for the processor, the bootstrap memory unit for storing a bootstrap program;

an interface to one of a reload memory unit with serial data access and a reload memory unit which requires a plurality of read access operations in order to read a program command for the processor the reload memory unit for storing program commands;

an interface to a main memory unit with parallel data access for reading a program command, wherein program commands from the reload memory unit are transferred to the main memory unit using the bootstrap program before execution by the processor; and

a control unit which in response to a start signal, prompts a bootstrap transfer operation for transferring program commands for the processor from the bootstrap memory unit to the main memory unit, and which, after the bootstrap transfer operation, prompts the processor to execute the program commands transferred to the main memory unit, and which permits a reload transfer operation in which program commands are transferred from the reload memory unit to the main memory unit.

Claims 17-19 (canceled).

Claim 20 (new): A process for starting a data processing installation, the process comprising the steps of:

storing program commands of a bootstrap program in a bootstrap memory unit;

transmitting the program commands of the bootstrap program, in a bootstrap transmission process, from the bootstrap memory unit into an initial area of a main memory unit using a control circuit;

copying the program commands of the bootstrap program, via a processor, from the initial area into an end area of the main memory unit;

starting execution of the program commands transmitted into the main memory unit, via the processor, during the bootstrap transmission process wherein a reload transfer operation is executed for transmitting program instructions from a reload memory unit into the initial area of the main memory unit;

wherein at least one of the bootstrap memory unit and the reload memory unit is one of a serial-access memory unit and a memory unit which requires a plurality of read access operations in order to read a program command for the processor.